

#6
CB
02-04-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei YAMAZAKI et al. Art Unit : 2871
Serial No. : 09/809,646 Examiner : Unknown
Filed : March 16, 2001
Title : SEMICONDUCTOR DISPLAY DEVICE AND MANUFACTURING METHOD
THEREOF

Commissioner for Patents
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Applicants submit the references listed on the attached form PTO-1449, copies of which are enclosed.

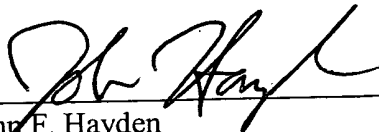
Submitted herewith is an English translation of the following foreign language references, or portions thereof:

Desig. ID	Source
AL	Japan 07-130652: Patent Abstract; 2001 JPO & JAPIO; File 347; No. 04838052
AM	Japan 08-78329: Patent Abstract; 2001 JPO & JAPIO; File 347; No. 05122829
AN	Japan 10-135468: Patent Abstract; 1999 JPO & JAPIO; File 347; No. 05852368
AO	Japan 10-135469: Patent Abstract; 1999 JPO & JAPIO; File 347; No. 05852369

This statement is being filed before the receipt of a first Office action on the merits.
Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 15, 2002


John F. Hayden
Reg. No. 37,640

Fish & Richardson P.C.
601 Thirteenth Street, NW
Washington, DC 20005
Telephone: (202) 783-5070
Facsimile: (202) 783-2331